

K S GROUP OF INSTITUTIONS
K S SCHOOL OF ENGINEERING AND MANAGEMENT
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Remedial Classes Time Table (After 1st IA)

Sem: 5 'A' Sec

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	17ES51	MANAGEMENT AND ENTREPRENEURSHIP DEVELOPMENT	Mrs. Gunasheela T J	24/9/2019	6	
2	17EC52	DIGITAL SIGNAL PROCESSING	Mr. Senthil Babu	26/9/2019	7	
3	17EC53	VERILOG HDL	Mrs. Shanthala G M	1/10/2019	6	
4	17EC54	INFORMATION THEORY AND CODING	Mr. Manu D K	1/10/2019	7	
5	17EC55X	PROFESSIONAL ELECTIVE-1 (OPERATING SYSTEM)	Dr. Karthik P	24/9/2019	7	
6	17EC56X	ELECTIVE -2 (AUTOMOTIVE ELETRONICS, C++)	Mr. Sanjay B Nayak/ Mr. Arun Kumar	3/10/2019	7	

Sem: 5 'B' Sec

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	17ES51	MANAGEMENT AND ENTREPRENEURSHIP DEVELOPMENT	Mr. M S Prabhu	23/9/2019	7	
2	17EC52	DIGITAL SIGNAL PROCESSING	Mr. Senthil Babu	25/9/2019	7	
3	17EC53	VERILOG HDL	Mrs. Renuka V Tali	27/9/2019	7	
4	17EC54	INFORMATION THEORY AND CODING	Mr. Manu D K	30/9/2019	7	
5	17EC55X	PROFESSIONAL ELECTIVE-1 (OPERATING SYSTEM, MSP430)	Dr. Karthik P	4/10/2019	6	
6	17EC56X	ELECTIVE -2 (AUTOMOTIVE ELETRONICS, C++)	Mr. Sanjay B Nayak/ Ms. Zaiba	4/10/2019	7	



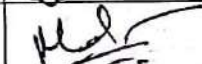
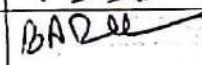

Time Table Co-ordinator

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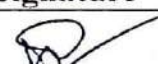
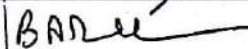
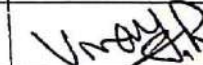


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING


Remedial Classes Time Table (After 1st IA)


Sem: 7 'A' Sec

Sl. No.	Subject Code	Subject	Hour	Faculty Name	Date	Hour	Signature
1	15EC71	MICROWAVE & ANTENNA	6	Mr. Puneeth S	24/9/2019	6	
2	15EC72	DIGITAL IMAGE PROCESSING	7	Dr. Dushayanth N D	26/9/2019	7	
3	15EC73	POWER ELECTRONICS		Mr. Arun Kumar	24/9/2019	5	
4	15EC74X	ELECTIVE -1 (REAL TIME SYSTEMS/ MULTIMEDIA COMMUNICATION)	5	Dr. Dushyanti/ Mr. Ravikiran B A	26/9/2019	5	
5	15EC75X	ELECTIVE -2 (SATELLITE COMMUNICATION)		Mrs. Kripa	26/9/2019	6	

Sem: 7 'B' Sec





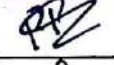
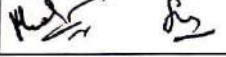
Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	15EC71	MICROWAVE & ANTENNA	Mrs. Gunasheela T J	23/9/2019	7	
2	15EC72	DIGITAL IMAGE PROCESSING	Mr. Ravikiran	27/9/2019	5	
3	15EC73	POWER ELECTRONICS	Mr. Vinay R	23/9/2019	6	
4	15EC74X	ELECTIVE -1 (REAL TIME SYSTEMS/ MULTIMEDIA COMMUNICATION)	Dr. Dushyanti/ Mr. Ravikiran B A	27/9/2019	6	
5	15EC75X	ELECTIVE -2 (SATELLITE COMMUNICATION)	Dr. Girish V Attimarad	27/9/2019	7	


Time Table Co-ordinator


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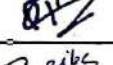

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Remedial Classes Timetable (After 2nd IA)

Sem: 5 'A' Sec

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	17ES51	MANAGEMENT AND ENTREPRENEURSHIP DEVELOPMENT	Mrs. Gunasheela T J	7/11/2019	6	
2	17EC52	DIGITAL SIGNAL PROCESSING	Mr. Senthil Babu	12/11/2019	6	
3	17EC53	VERILOG HDL	Mrs. Shanthala G M	19/11/2019	6	
4	17EC54	INFORMATION THEORY AND CODING	Mr. Manu D K	12/11/2019	7	
5	17EC55X	PROFESSIONAL ELECTIVE-1 (OPERATING SYSTEM)	Dr. Karthik P	7/11/2019	7	
6	17EC56X	ELECTIVE -2 (AUTOMOTIVE ELETRONICS, C++)	Mr. Sanjay B Nayak/ Mr. Arun Kumar	19/11/2019	7	

Remedial Classes Timetable

Sem: 5 'B' Sec

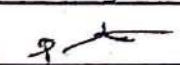

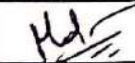
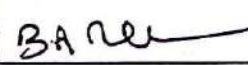

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	17ES51	MANAGEMENT AND ENTREPRENEURSHIP DEVELOPMENT	Mr. M S Prabhu	13/11/2019	6	
2	17EC52	DIGITAL SIGNAL PROCESSING	Mr. Senthil Babu	13/11/2019	7	
3	17EC53	VERILOG HDL	Mrs. Renuka V Tali	8/11/2019	6	
4	17EC54	INFORMATION THEORY AND CODING	Mr. Manu D K	11/11/2019	7	
5	17EC55X	PROFESSIONAL ELECTIVE-1 (OPERATING SYSTEM, MSP430)	Dr. Karthik P	11/11/2019	6	
6	17EC56X	ELECTIVE -2 (AUTOMOTIVE ELETRONICS, C++)	Mr. Sanjay B Nayak/ Ms. Zaiba	8/11/2019	7	


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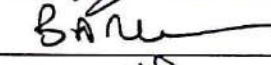
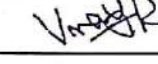


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Remedial Classes Timetable

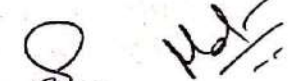
Sem: 7 'A' Sec


Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	15EC71	MICROWAVE & ANTENNA	Mr. Puneeth S	19/11/2019	6	
2	15EC72	DIGITAL IMAGE PROCESSING	Dr. Dushyanth N D	14/11/2019	5	
3	15EC73	POWER ELECTRONICS	Mr. Arun Kumar	19/11/2019	7	
4	15EC74X	ELECTIVE -1 (REAL TIME SYSTEMS/ MULTIMEDIA COMMUNICATION)	Dr. Dushyanth/ Mr. Ravikiran B A	9/11/2019	6	
5	15EC75X	ELECTIVE -2 (SATELLITE COMMUNICATION)	Mrs. Kripa	9/11/2019	7	

Remedial Classes Timetable

Sem: 7 'B' Sec

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	15EC71	MICROWAVE & ANTENNA	Mrs. Gunasheela T J	13/11/2019	7	
2	15EC72	DIGITAL IMAGE PROCESSING	Mr. Ravikiran	13/11/2019	6	
3	15EC73	POWER ELECTRONICS	Mr. Vinay R	11/11/2019	6	
4	15EC74X	ELECTIVE -1 (REAL TIME SYSTEMS/ MULTIMEDIA COMMUNICATION)	Dr. Dushyanth/ Mr. Ravikiran B A	11/11/2019	7	
5	15EC75X	ELECTIVE -2 (SATELLITE COMMUNICATION)	Dr. Girish V Attimarad	18/11/2019	7	


Time Table Co-ordinator


HOD
6/11/19

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Remedial Classes Timetable

Sem: 3 'A' Sec

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	18MAT31	MATHEMATICS	Mrs. Nagarathna T.K	7/11/2019	6	
2	18EC32	NETWORK THEORY	Mrs. Shalini Shravan	14/11/2019	6	
3	18EC33	ELECTRONICS DEVICES	Mrs. Deepa R Bhangi	7/11/2019	7	
4	18EC34	DIGITAL SYSTEM DESIGN	Mrs. Shilpa A P	19/11/2019	7	
5	18EC35	COMPUTER ORGANIZATION & ARCHITECTURE	Dr. Kishore M	12/11/2019	7	
6	18EC36	POWER ELETRONICS & INSTRUMENTATION	Mr. Gopal KrishnaMurthy C R	14/11/2019	7	

NOTE: Following alternate arrangements has been made to accommodate remedial classes for 'A' section

7/11/2019	Network Theory 1.20 PM to 2.10 PM Regular class; Maths 2.10 PM to 3.00 PM remedial class
19/11/2019	Power Electronics & Instrumentation 10.45 AM to 11.40 AM Regular class; Electronic Devices 3.00 PM to 3.50 PM Remedial Class
	Computer Organization 9.35 AM to 10.30 AM Regular Class; Digital System Design 3.00 PM to 3.50 PM Remedial Class

Remedial Classes Timetable

Sem: 3 'B' Sec

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	18MAT31	MATHEMATICS	Ms. Divya R.	13/11/2019	6	
2	18EC32	NETWORK THEORY	Mr. Puneeth S	8/11/2019	7	
3	18EC33	ELECTRONICS DEVICES	Mr. Vinay R	13/11/2019	7	
4	18EC34	DIGITAL SYSTEM DESIGN	Mrs. Renuka V Tali	11/11/2019	7	
5	18EC35	COMPUTER ORGANIZATION & ARCHITECTURE	Dr. Kishore M	11/11/2019	6	
6	18EC36	POWER ELETRONICS & INSTRUMENTATION	Mr. Gopal KrishnaMurthy C R	8/11/2019	6	

NOTE: Following alternate arrangements has been made to accommodate remedial classes for 'B' section

13/11/19	Digital System Design 1.20 pm to 2.10 pm Regular class; Maths 2.10 to 3.00 pm Remedial class
8/11/2019	Computer Organization 9.35 AM to 10.30 AM Regular Class; Power Electronics & Instrumentation 2.10 PM to 3.00 PM Remedial Class

Time Table Co-ordinator

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6/11/19

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Remedial Classes Time Table (After 1st IA)

Sem: 3 'A' Sec

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	18MAT31	MATHEMATICS	Mrs. Nagarathna T.K	26/9/2019	6	
2	18EC32	NETWORK THEORY	Mrs. Shalini Shravan	26/9/2019	7	
3	18EC33	ELECTRONICS DEVICES	Mrs. Deepa R Bhangi	1/10/2019	7	
4	18EC34	DIGITAL SYSTEM DESIGN	Mrs. Shilpa A P	3/10/2019	6	
5	18EC35	COMPUTER ORGANIZATION & ARCHITECTURE	Dr. Kishore M	24/9/2019	7	
6	18EC36	POWER ELETRONICS & INSTRUMENTATION	Mr. Gopal KrishnaMurthy C R	3/10/2019	7	

Sem: 3 'B' Sec

Sl. No.	Subject Code	Subject	Faculty Name	Date	Hour	Signature
1	18MAT31	MATHEMATICS	Ms. Divya R.	27/9/2019	7	
2	18EC32	NETWORK THEORY	Mr. Puneeth S	30/9/2019	7	
3	18EC33	ELECTRONICS DEVICES	Mr. Vinay R	4/10/2019	6	
4	18EC34	DIGITAL SYSTEM DESIGN	Mrs. Renuka V Tali	23/9/2019	7	
5	18EC35	COMPUTER ORGANIZATION & ARCHITECTURE	Dr. Kishore M	27/9/2019	6	
6	18EC36	POWER ELETRONICS & INSTRUMENTATION	Mr. Gopal KrishnaMurthy C R	4/10/2019	7	

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 2/10/19