



K.S. SCHOOL OF ENGINEERING AND MANAGEMENT, BENGALURU-560109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

SESSION: YEAR : 2022-2023 (ODD SEMESTER)

CLASS TIME TABLE

(w.e.f 31st OCT-2022)

CLASS : III 'A' ECE

Class Teacher:Mr. Syed Waseem Tabraiz

Room No:A209

| DAY | 8:40-9:35 | 9:35-10:30 | 10:30-10:45 | 10:45-11:40 | 11:40-12:35 | 12:35-1:20 | 1:20-2:10 | 2:10-3:00 | 3:00-3:50 | |
|-----------|------------|------------|-------------|-------------|-------------|-------------|------------------------------------------------------------|-----------|-----------|--|
| MONDAY | AEC | DSDV | TEA BREAK | MATHS | CPE | LUNCH BREAK | A1 BATCH-C1 CYCLE (CRG/VJK) A2 BATCH-C2 CYCLE (SWT/TGV) | | | |
| TUESDAY | MATHS | BSP | | AEC-3 | | | AEC | DSDV | LIBRARY | |
| WEDNESDAY | DSDV | AEC | | BSP | MATHS | | A2 BATCH-C1 CYCLE (CRG/VJK) A3 BATCH-C2 CYCLE | | DIP MATH | |
| THURSDAY | AEC | TUTORIAL | | DSDV | BSP | | MATHS | SCR | DIP MATH | |
| FRIDAY | BSP | MATHS | | AEC | BSP | | A3 BATCH-C1 CYCLE (CRG/TGV) A1 BATCH-C2 CYCLE (SWT/HM) | | | |
| SATURDAY | As Per COE | | | As Per COE | | | As Per COE | | | |

| CODE | SUBJECT | HOURS /WEEK | STAFF |
|---------|-----------------------------------------------|-------------|-------------------------------------------------------------------|
| 21MAT31 | MATHEMATICS COURSE | 5 | Mr.Sudhakar P |
| 21EC32 | DIGITAL SYSTEM DESIGN USING VERILOG | 4 | Mr. Syed Waseem Tabraiz |
| 21EC33 | BASIC SIGNAL PROCESSING | 5 | Mrs. Jayashree G R |
| 21EC34 | ANALOG ELECTRONICS CIRCUITS | 5 | Mr. Gopala Krishna Murthy C R |
| 21ECL35 | ANALOG AND DIGITAL ELECTRONICS LAB | 9 | Mr. Gopala Krishna Murthy C R/Mr. Syed Waseem Tabraiz |
| 21EC38X | ABILITY ENHANCEMENT COURSE-3 | 2 | Mrs. Bhargavi Vijendra Sangam/Mr.Ravikiran B A/Mr. Sanjay B Nayak |
| 21SCR36 | SOCIAL CONNECT & RESPONSIBILITIES | 1 | Mrs.Vijayalakshmi Kalal |
| 21CIP37 | CONSTITUTION OF INDIA AND PROFESSIONAL ETHICS | 1 | Mrs.Ashwini |

Time Table Co-ordinator

Professor & Head

Dept. of Electronics & Communication Engineering
 K. S. School of Engineering & Management
 Bangalore-560 109

Principal

Dr. K. RAMA NARASIMHA
 Principal/Director
 K S School of Engineering and Management
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SESSION: YEAR : 2022-2023 (ODD SEMESTER)

CLASS TIME TABLE

(w.e.f. 31st OCT-2022)

CLASS : III 'B' ECE

Class Teacher: Mrs. Renuka V Tali

Room No:A209A

| DAY | 8:40-9:35 | 9:35-10:30 | 10:30-10:45 | 10:45-11:40 | 11:40-12:35 | 12:35-1:20 | 1:20-2:10 | 2:10-3:00 | 3:00-3:50 |
|-----------|------------|-------------------------------------------------------------|-------------|-------------|-------------|------------|--------------------------------------------------------------|-----------|-----------|
| MONDAY | DSDV | B1 BATCH- C1 CYCLE (BVS/TGV) B2 BATCH -C2 CYCLE (MDK/HM) | | | LUNCH BREAK | MATHS | AEC | CPE | |
| TUESDAY | BSP | MATHS | TEA BREAK | DSDV | | TUTORIAL | B2 BATCH- C1 CYCLE (BVS/MBG) B3 BATCH -C2 CYCLE(MDK/TGV) | | |
| WEDNESDAY | AEC | MATHS | | AEC-3 | | DSDV | BSP | DIP MATHS | |
| THURSDAY | AEC | BSP | | MATHS | | AEC | B3 BATCH- C1 CYCLE (BVS/MBG) B1 BATCH -C2 CYCLE (MDK/VJK) | | |
| FRIDAY | MATHS | BSP | | AEC | | DSDV | BSP | LIBRARY | SCR |
| SATURDAY | As Per COE | | | As Per COE | | As Per COE | | | |

| CODE | SUBJECT | HOURS /WEEK | STAFF |
|---------|-----------------------------------------------|-------------|--------------------------------------------------------------------|
| 21MAT31 | MATHEMATICS COURSE | 5 | Mrs. Vinutha S V |
| 21EC32 | DIGITAL SYSTEM DESIGN USING VERILOG | 4 | Mrs. Renuka V Tali |
| 21EC33 | BASIC SIGNAL PROCESSING | 5 | Mr. Ravikiran B A |
| 21EC34 | ANALOG ELECTRONICS CIRCUITS | 5 | Mrs. Bhargavi Vijendra Sangam |
| 21ECL35 | ANALOG AND DIGITAL ELECTRONICS LAB | 9 | Mr. Gopala Krishna Murthy C R/Mr. Syed Waseem Tabraiz |
| 21EC38X | ABILITY ENHANCEMENT COURSE-3 | 2 | Mrs. Bhargavi Vijendra Sangam/Mr. Ravikiran B A/Mr. Sanjay B Nayak |
| 21SCR36 | SOCIAL CONNECT & RESPONSIBILITIES | 1 | Mrs. Vijayalaksmi Kalal |
| 21CIP37 | CONSTITUTION OF INDIA AND PROFESSIONAL ETHICS | 1 | Mrs. Ashwini |

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CLASS TIME TABLE

(w.e.f. 10th OCT-2022)

CLASS : V ECE

Class Teacher: Mr. Dileep J

Room No:A313

| DAY | 8:40-9:35 | 9:35-10:30 | 10:30-10:45 | 10:45-11:40 | 11:40-12:35 | 12:35-1:20 | 1:20-2:10 | 2:10-3:00 | 3:00-3:50 |
|-----------|------------|------------|-------------|-------------|-------------|-------------|-------------------------------------------------|-----------|-----------|
| MONDAY | TIM&E | DSP | TEA BREAK | PCS | EMW | LUNCH BREAK | DSP A1 BATCH (AKM/JGR) HDL A2 BATCH (KM/RVT) | | |
| TUESDAY | ITC | EMW | | PCS | DSP | | TIM&E | ITC | LIBRARY |
| WEDNESDAY | EMW | DSP | | VHDL | ITC | | DSP A2 BATCH (AKM/JGR) HDL A3 BATCH (KM/RVT) | | |
| THURSDAY | PCS | VHDL | | EMW | TIM&E | | DSP | VHDL | EVS |
| FRIDAY | TIM&E | VHDL | | ITC | PCS | | DSP A3 BATCH (AKM/JGR) HDL A1 BATCH (KM/RVT) | | |
| SATURDAY | As Per COE | | | As Per COE | | | As Per COE | | |

| CODE | SUBJECT | HOURS /WEEK | STAFF |
|---------|-----------------------------------------------------------|-------------|-------------------------------------|
| 18ES51 | TECHNOLOGICAL INNOVATION MANAGEMENT & ENTREPRENEURSHIP | 4 | Mr. Sanjay B Nayak |
| 18EC52 | DIGITAL SIGNAL PROCESSING | 4 | Dr. Senthil Babu K |
| 18EC53 | PRINCIPLES OF COMMUNICATION SYSTEMS | 4 | Dr. Manu D K |
| 18EC54 | INFORMATION THEORY & CODING | 4 | Dr. Kishore M |
| 18EC55 | ELECTROMAGNETIC WAVES | 4 | Mr. Dileep J |
| 18EC56 | VERILOG HDL | 4 | Mrs. Renuka V Tall |
| 18ECL57 | DIGITAL SIGNAL PROCESSING LABORATORY | 9 | Dr. Arunkumar M/ Mrs. Jayashree G R |
| 18ECL58 | HDL LABORATORY | 9 | Dr. Kishore M/ Mrs. Renuka V Tall |
| 18CIV59 | ENVIRONMENTAL STUDIES | 1 | Dr. Radha H R |

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SESSION: YEAR : 2022-2023 (ODD SEMESTER)

CLASS TIME TABLE

(w.e.f. 19th SEPT-2022)

CLASS : VII ECE 'A'

Class Teacher: Dr. Kishore M

Room No: 310

| DAY | 8:40-9:35 | 9:35-10:30 | 10:30-10:45 | 10:45-11:40 | 11:40-12:35 | 12:35-1:20 | 1:20-2:10 | 2:10-3:00 | 3:00-3:50 |
|-----------|------------|------------|-------------|-------------|---------------|-------------|-------------------------------------------------|-----------|-----------|
| MONDAY | CN | ELECTIVE-2 | TEA BREAK | VLSI | OPEN ELECTIVE | LUNCH BREAK | VLSI A1 BATCH (DJ/DB) CCN A2 BATCH (SS/KSB) | | |
| TUESDAY | VLSI | CN | | ELECTIVE-3 | ELECTIVE-2 | | PROJECT PHASE -1 | | |
| WEDNESDAY | ELECTIVE-2 | ELECTIVE-3 | | CN | VLSI | | VLSI A2 BATCH (DB/SAP) CCN A3 BATCH (KSB/DJ) | | |
| THURSDAY | ELECTIVE-2 | VLSI | | ELECTIVE-3 | OPEN ELECTIVE | | TUTORIAL | TUTORIAL | TUTORIAL |
| FRIDAY | ELECTIVE-3 | CN | | LIBRARY | OPEN ELECTIVE | | VLSI A3 BATCH (DB/SAP) CCN A1 BATCH (SS/DJ) | | |
| SATURDAY | As Per COE | | | As Per COE | | | As Per COE | | |

| CODE | SUBJECT | HOURS/WEEK | STAFF |
|-------------|-----------------------------------|------------|------------------------------------------------------|
| 18EC71 | COMPUTER NETWORKS | 4 | Mrs. Swati Sarkar |
| 18EC72 | VLSI DESIGN | 4 | Mrs. Shilpa A P |
| 18EC732/733 | PROFESSIONAL ELECTIVE-2 (SATCOM) | 4 | Mrs. Jayashree G R |
| 18EC743/745 | PROFESSIONAL ELECTIVE-3 (MMC/MLP) | 4 | Dr. Kishore M |
| 18EC75X | OPEN ELECTIVE | 3 | Faculty from Other Departments. |
| 18ECL76 | COMPUTER NETWORKS LAB | 9 | Mrs. Swati Sarkar/Dr. Senthil Babu K/Mr. Ravikiran B |
| 18ECL77 | VLSI LABORATORY | 9 | Mr. Dileep J/Mrs. Deepa Bangi/Mrs. Shilpa A P |
| 18ECP78 | PROJECT WORK PHASE-I | 3 | Dr. K Senthil Babu/ Mr. Sanjay B Nayak |

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CLASS TIME TABLE

(w.e.f. 19th SEPT-2022)

CLASS : VII ECE 'B'

Class Teacher: Mrs.Deepa Bangl

Room No:311

| DAY | 8:40-9:35 | 9:35-10:30 | 10:30-10:45 | 10:45-11:40 | 11:40-12:35 | 12:35-1:20 | 1:20-2:10 | 2:10-3:00 | 3:00-3:50 |
|-----------|------------------------------------------------|------------|-------------|-------------|---------------|-------------|----------------------------------------------------|------------|-----------|
| MONDAY | VLSI B1 BATCH (DB/KP) CCN B2 BATCH (RBA/DJ) | | | | OPEN ELECTIVE | LUNCH BREAK | ELECTIVE-2 | ELECTIVE-3 | LIBRARY |
| TUESDAY | CN | VLSI | TEA BREAK | ELECTIVE-3 | ELECTIVE-2 | | VLSI B2 BATCH (DJ/DB/SAP) CCN B3 BATCH (RBA/SS) | | |
| WEDNESDAY | VLSI | ELECTIVE-2 | | ELECTIVE-3 | CN | | TUTORIAL | TUTORIAL | TUTORIAL |
| THURSDAY | ELECTIVE-3 | CN | | VLSI | OPEN ELECTIVE | | VLSI B3 BATCH (DJ/DB/KP) CCN B1 BATCH (KSB/RBA) | | |
| FRIDAY | ELECTIVE-2 | VLSI | | CN | OPEN ELECTIVE | | PROJECT PHASE -I | | |
| SATURDAY | As Per COE | | | As Per COE | | | As Per COE | | |

| CODE | SUBJECT | HOURS /WEEK | STAFF |
|-------------|-----------------------------------|-------------|-----------------------------------------------------|
| 18EC71 | COMPUTER NETWORKS | 4 | Dr. Senthil Babu K |
| 18EC72 | VLSI DESIGN | 4 | Mrs.Deepa Bangl |
| 18EC732/733 | PROFESSIONAL ELECTIVE-2 (SATCOM) | 4 | Mrs. Shilpa A P |
| 18EC743/745 | PROFESSIONAL ELECTIVE-3 (MMC/MLP) | 4 | Dr. Karthik P |
| 18EC75X | OPEN ELECTIVE | 3 | Faculty from Other Departments. |
| 18ECL76 | COMPUTER NETWORKS LAB | 9 | Mrs. Swati Sarkar/Dr. Senthil Babu K/Mr.Ravikiran B |
| 18ECL77 | VLSI LABORATORY | 9 | Mr. Dileep J/Mrs.Deepa Bangl/Mrs. Shilpa A P |
| 18ECP78 | PROJECT WORK PHASE-I | 3 | Dr.K Senthil Babu/ Mr. Sanjay B Nayak |

[Signature]
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(w.e.f. 19th SEPT-2022)

| CLASS : VII ECE 'A' | Parallel Batch | | Class Teacher: Dr. Kishore M | | | | | | Room No: A211 |
|---------------------|----------------|------------|------------------------------|-------------|-------------|-------------|-------------------|-----------|---------------|
| DAY | 8:40-9:35 | 9:35-10:30 | 10:30-10:45 | 10:45-11:40 | 11:40-12:35 | 12:35-1:20 | 1:20-2:10 | 2:10-3:00 | 3:00-3:50 |
| MONDAY | DIP | SATCOM | TEA BREAK | M&A | PE | LUNCH BREAK | VLSI LAB (DJ/DB) | | |
| TUESDAY | PE | DIP | | MMC | SATCOM | | M&A | LIBRARY | TUTORIAL |
| WEDNESDAY | SATCOM | MMC | | M&A | PE | | ADC LAB (GVA/KP) | | |
| THURSDAY | SATCOM | M&A | | MMC | DIP | | TUTORIAL | TUTORIAL | TUTORIAL |
| FRIDAY | MMC | PE | | DIP | M&A | | PROJECT PHASE - I | | |
| SATURDAY | As Per COE | | | As Per COE | | | As Per COE | | |

| CODE | SUBJECT | HOURS /WEEK | STAFF |
|---------|---------------------------------------------|-------------|---------------------------------------|
| 17EC71 | MICROWAVE AND ANTENNAS | 5 | Dr. Girish V Attimarad |
| 17EC72 | DIGITAL IMAGE PROCESSING | 4 | Dr. Karthik P |
| 17EC73 | POWER ELECTRONICS | 4 | Mr. Gopala Krishna Murthy C R |
| 17EC741 | PROFESSIONAL ELECTIVE-3 (MMC) | 4 | Dr. Kishore M |
| 17EC755 | PROFESSIONAL ELECTIVE-4 (SATCOM) | 4 | Mrs. Jayashree G R |
| 17ECL76 | ADVANCED COMMUNICATION LAB | 3 | Dr. Girish V Attimarad/Dr. Karthik P |
| 17ECL77 | VLSI LAB | 3 | Mr. Dileep J/Mrs. Deepa Bangi |
| 17ECP78 | PROJECT WORK PHASE-I + PROJECT WORK SEMINAR | 3 | Dr.K Senthil Babu/ Mr. Sanjay B Nayak |

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