

K. S. School of Engineering and Management

Department of Electronics and Communication Engineering

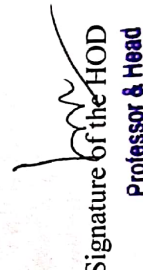
Third Internal Test Time Table April-July-2022

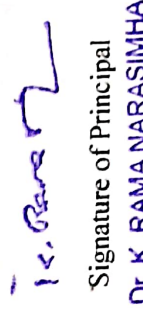
Semester- VI (Parallel Batch)

Date: 07/07/2022

Sl. No	Date	Day	Morning Session Time: 9.30 am To 11 am Subject (Code)	After Noon Session Time: 1.30 pm To 3.00 pm Subject (Code)
1	14/07/2022	Thursday	Digital Communications (17EC61)	ARM Microcontroller & Embedded Systems (17EC62)
2	15/07/2022	Friday	VLSI Design (17EC63)	Computer Communication Network (17EC64)
3	16/07/2022	Saturday	Professional Elective-2 (Digital Switching Systems) (17EC65X)	Open Elective - 1 (Digital System Design using Verilog) (17EC66X)


Signature of the Test Coordinator


Signature of the HOD


Signature of Principal

Professor & Head
Dept. of Electronics & Communication Engineering
K. S. School of Engineering & Management
Bangalore-560 109


Dr. K. RAMA NARASIMHA
Principal/Director
K S School of Engineering and Management
Bangalore - 560 109

K. S. School of Engineering and Management
Department of Electronics and Communication Engineering

Third Internal Test Time Table April-July-2022


Semester- VI

Sl. No	Date	Day	Date: 07/07/2022	
			Morning Session Time: 9.30 am To 11 am	After Noon Session Time: 1.30 pm To 3.00 pm
			Subject (Code)	Subject (Code)
1	14/07/2022	Thursday	Digital Communications (18EC61)	Embedded Systems (18EC62)
2	15/07/2022	Friday	Microwave & Antennas (18EC63)	Professional Elective-1 (18EC64X)
3	16/07/2022	Saturday	Open Elective-A (18EC65X)	


Signature of the Test Coordinator


Signature of the HOD

Professor & Head
Dept. of Electronics & Communication Engineering
K. S. School of Engineering & Management
Bangalore-560 109


Signature of Principal
Dr. K. RAMA NARASIMHA
Principal/Director
K S School of Engineering and Management
Bangalore - 560 109