

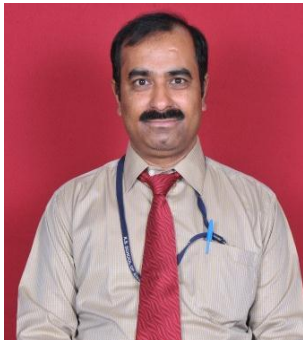


Kammavari Sangham (R) 1952, K. S. Group of Institutions

K. S. SCHOOL OF ENGINEERING & MANAGEMENT

No.15, Mallasandra, Off. Kanakapura Road, Bengaluru-560109

Affiliated to VTU, Belagavi & Approved by AICTE, New Delhi, **Accredited by NAAC**

Faculty Name	Mr. Gopalakrishna Murthy C R	
Designation	Associate Professor	
Educational Qualification	B.E., M.E., (Ph.D)	
Experience in Years	Teaching: 15 Industry : 1	
Areas of Interest	Power Electronics, Embedded Systems , Sensors	
Subjects Taught	Power Electronics, Network Theory Microprocessor/Controllers, Embedded System, Communication System and Wireless Communication, Analog Electronics	
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EDUCATIONAL DETAILS:-

Examination / Degree	College/University	Year of Passing
BE	SIT, Bangalore University	1999
M. Tech	UVCE, Bangalore/ Bangalore University	2007
PH.D	VTU (Pursuing)	

PUBLICATIONS:-

ORCID ID: [0000-0003-0186-7194](https://orcid.org/0000-0003-0186-7194)

Journal Publications:

1. "Parallel Processing For Using A Shared Memory In A Network With The Help of Context Switching Semaphore Using Self-Healing Approach" International Journal of Applied Engineering Research ISSN 0973-4562 Volume 10, Number 12 (2015) pp. 30371-30392.
2. Implementation of an Indoor Navigation Robot for Visually Impaired and New Visitors", i-manager's Journal on Embedded Systems, 6(1), pp:6-9, 2017.
3. "Development and Implementation of Highway Electric Power Generation Using Wind Turbine for Smart Traffic and Light Control System" i-manager's Journal on Embedded Systems, January – June 2019 , Volume No. 7, Issue No. 2.

Conference Papers :

1. "Design of a Cost Effective ATM Accessing System For The Blind using Hand Gestures on FPGA", Nation Conference on advanced Communication, VLSI Design and Signal Processing- NCCVS-2014.

2. "Implementation of AES Algorithm for image encryption using FPGA", National Conference on advanced Communication, VLSI Design and Signal Processing-NCCVS-2014.
3. "VLSI Implementation of Adaptive Noise Cancellation for Speech Signal using LMS and RLS Algorithms on FPGA", National Conference on advanced Communication, VLSI Design and Signal Processing-NCCVS-15.
4. "Design and Implementation of Robot Assisted Arduino Based Object Recognition and Sorting", International Conference on Applied Engineering Sciences and Management, KSSEM, 12th & 13th October 2018.

Professional Membership

1. Life time member of ISTE. LM-53736
2. International Association of Engineers (IAENG) 113606

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